( ) Preliminary Specification
(V) Final Specification

Module	19.0” SXGA Color TFT-LCD
Model Name	M190EG02 V8

Customer	**Date**	**Checked & Approved by**	**Date**
		Paul Huang	July. 30, 2010
Approved by		**Prepared by**	
		BD Lee	July. 30, 2010

Note: This Specification is subject to change without notice.

Desktop Display Business Group /
AU Optronics corporation

Contents

Record of Revision

Version & Date	Page	Old Description	New Description	Remark
0.1 2010/08/04	All	Frist Edition for Customer		

1. Handling Precautions

1) Since front polarizer is easily damaged, pay attention not to scratch it.
2) Be sure to turn off power supply when inserting or disconnecting from input connector.
3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
7) Do not open or modify the Module Assembly.
8) Do not press the reflector sheet at the back of the module to any directions.
9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the Cold Cathode Fluorescent Lamp (CCFL) reflector edge. Instead, press at the far ends of the CCFL reflector edge softly. Otherwise the TFT Module may be damaged.
10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
11) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
12) Cold Cathode Fluorescent Lamp (CCFL) in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
14) The LCD module is designed so that the CCFL in it is supplied by Limited Current Circuit (IEC60950 or UL1950). Do not connect the CCFL in Hazardous Voltage Circuit.

2. General Description

M190EG02 V8 is a Color Active Matrix Liquid Crystal Display composed of a TFT-LCD panel, a driver circuit, and a backlight system. The screen format is intended to support the SXGA (1280(H) x 1024(V)) screen and 16.7M colors (RGB 6-bits + HiFRC data). All input signals are 2-channel LVDS interface compatible. Inverter card of backlight is not included. M190EG02 V8 is designed for a display unit of personal computer.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 ℃ condition:

Items	Unit	Specifications
Screen Diagonal	[mm]	482.6 (19.0")
Active Area	[mm]	376.32 (H) x 301.06 (V)
Pixels H x V		1280(x3) x 1024
Pixel Pitch	[mm]	0.294 (per one triad) x 0.294
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		Normally White
White Luminance	[cd/m^2]	250 (center, Typ) @ 8.0mA
Contrast Ratio		1000 : 1 (Typ)
Optical ResponseTime	[msec]	5 ms(Typ, on/off)
Nominal Input Voltage VDD	[Volt]	+5.0 V
Power Consumption	[Watt]	14.3 W (Typ) (PDD= 4.6 W, PCFL=9.7 W @Lamp=8.0mA)
Weight	[Grams]	2300 (Typ)
Physical Size (H x V x D)	[mm]	396 (H) x 324 (V) x 16.3 (D) (Typ)
Electrical Interface		Dual channel LVDS
Surface Treatment		Hard-coating (3H), Non-Glare treatment
Support Color		16.7M colors (RGB 6-bit data + HiFRC data)
Temperature Range Operating Storage (Non-Operating)	 [°C] [°C]	 0 to +50 -20 to +60
RoHS Compliance		RoHS Compliance
Digital Gamma turn on Compliance		TCO5.0 Compliance

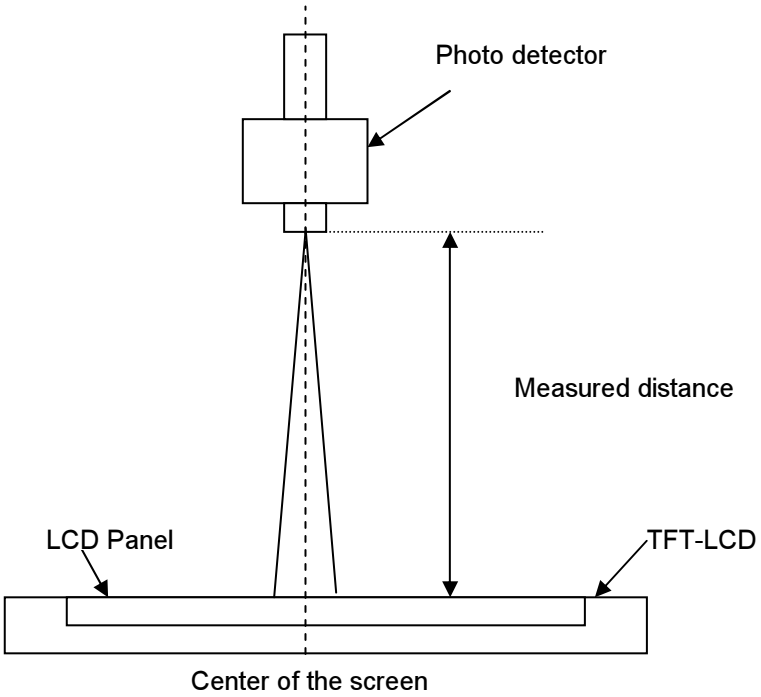
2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25℃ (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.	Note
Viewing Angle	[degree]	Horizontal (Right) CR = 10 (Left)	75 75	85 85	-	2
		Vertical (Up) CR = 10 (Down)	70 70	80 80	-	
		Horizontal (Up) CR = 5 (Down)	75 75	85 85	-	
		Vertical (Up) CR = 5 (Down)	75 75	85 85	-	
Contrast Ratio		Normal Direction	600	1000	-	3
Optical Response Time	[msec]	Raising Time (TrR)	-	3.6	5.7	4
		Falling Time (TrF)	-	1.4	2.3	
		Rising + Falling	-	5	8	
Color / Chromaticity Coordinates (CIE)		Red x	0.617	0.647	0.677	5
		Red y	0.310	0.340	0.370	
		Green x	0.258	0.288	0.318	
		Green y	0.575	0.605	0.635	
		Blue x	0.115	0.145	0.175	
		Blue y	0.041	0.071	0.101	
		White x	0.283	0.313	0.343	
		White y	0.299	0.329	0.359	
White Luminance (At CCFL= 8.0 mA)	[cd/m^2]		200	250	-	6
Luminance Uniformity	[%]	9 Points	75	80	-	7
Cross Talk (At 60Hz)	[%]		-	-	1.5	8
Flicker	[dB]		-	-	-20	9

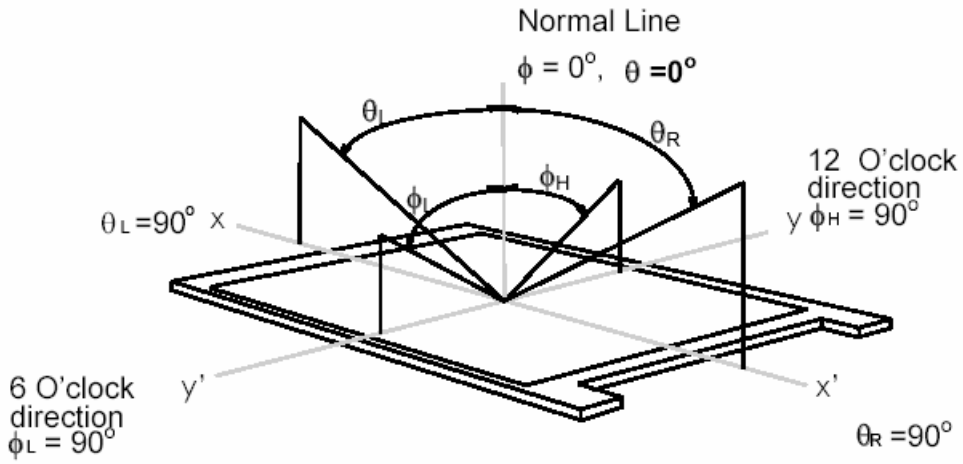
Note 1: **Measurement method**

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface 35□). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.



Note 2: **Definition of viewing angle** measured by ELDIM (EZContrast 88)

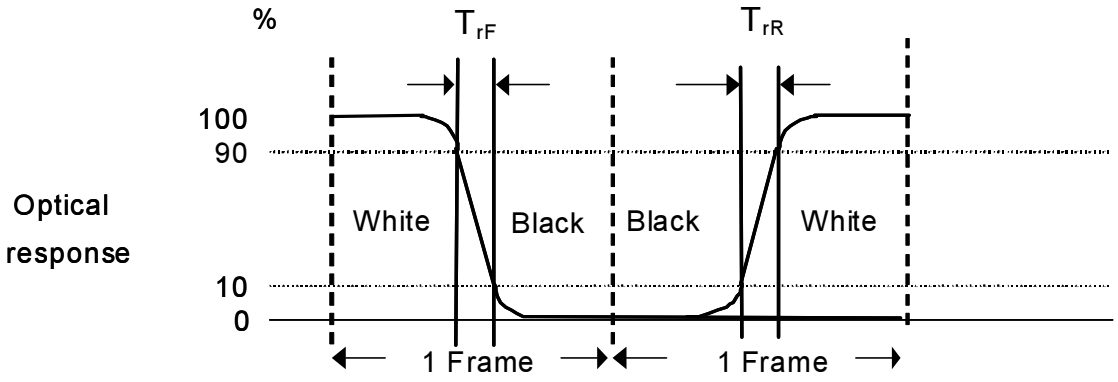
Viewing angle is the measurement of contrast ratio ≧10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (Φ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.



Note 3: **Contrast ratio is** measured by TOPCON SR-3

Note 4: **Definition of Response time** measured by Westar TRD-100A

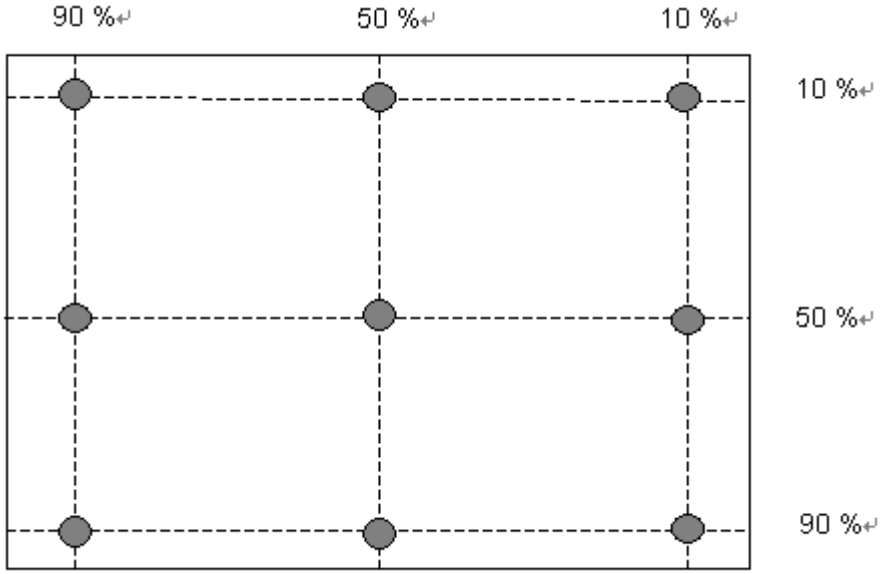
The output signals of photo detector are measured when the input signals are changed from “Full Black” to “Full White” (rising time, T_{rR}), and from “Full White” to “Full Black” (falling time, T_{rF}), respectively. The response time is interval between the 10% and 90% (1 frame at 60 Hz) of amplitudes.
$T_{rR} + T_{rF}$ = 5 msec (typ.).



Note 5: **Color chromaticity and coordinates (CIE) is** measured by TOPCON SR-3

Note 6**: Central luminance is** measured by TOPCON SR-3

Note 7: **Luminance uniformity of these 9 points is defined as below and** measured by TOPCON SR-3



$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 points (1 - 9)}}{\text{Maximum Luminance in 9 Points (1 - 9)}}$$

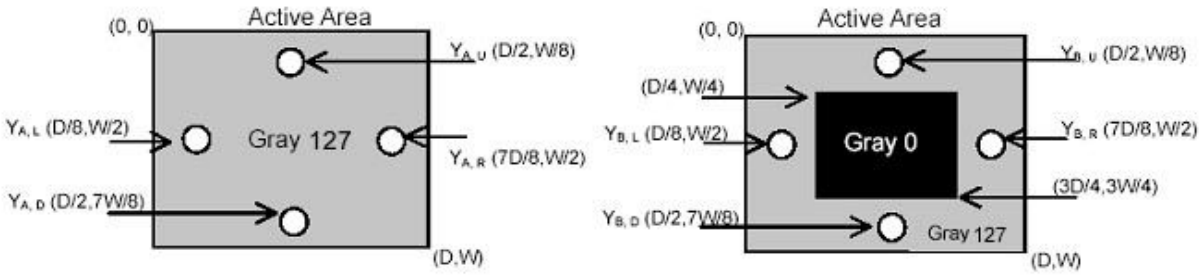
Note 8: **Crosstalk is defined as below and** measured by TOPCON SR-3

CT = | YB – YA | / YA × 100 (%)

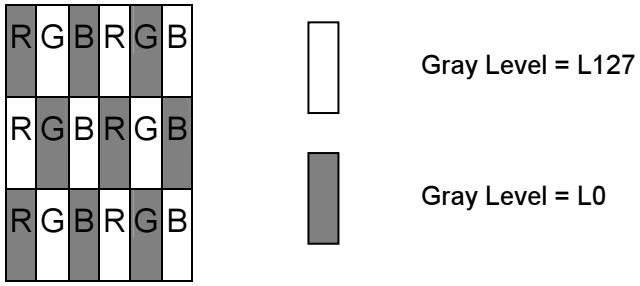
Where

YA = Luminance of measured location without gray level 0 pattern (cd/m2)

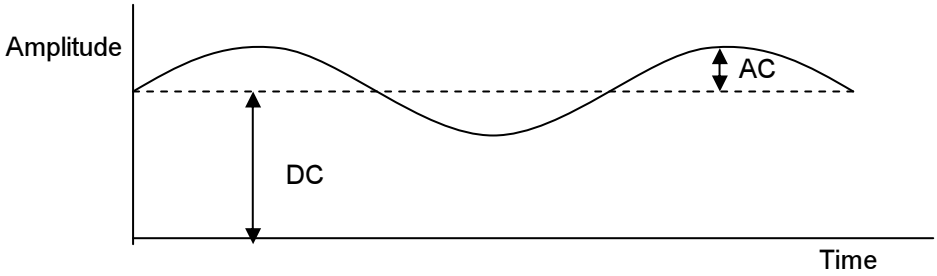
YB = Luminance of measured location with gray level 0 pattern (cd/m2)



Note 9: **Test Patern: Subchecker Pattern** measured by TOPCON SR-3



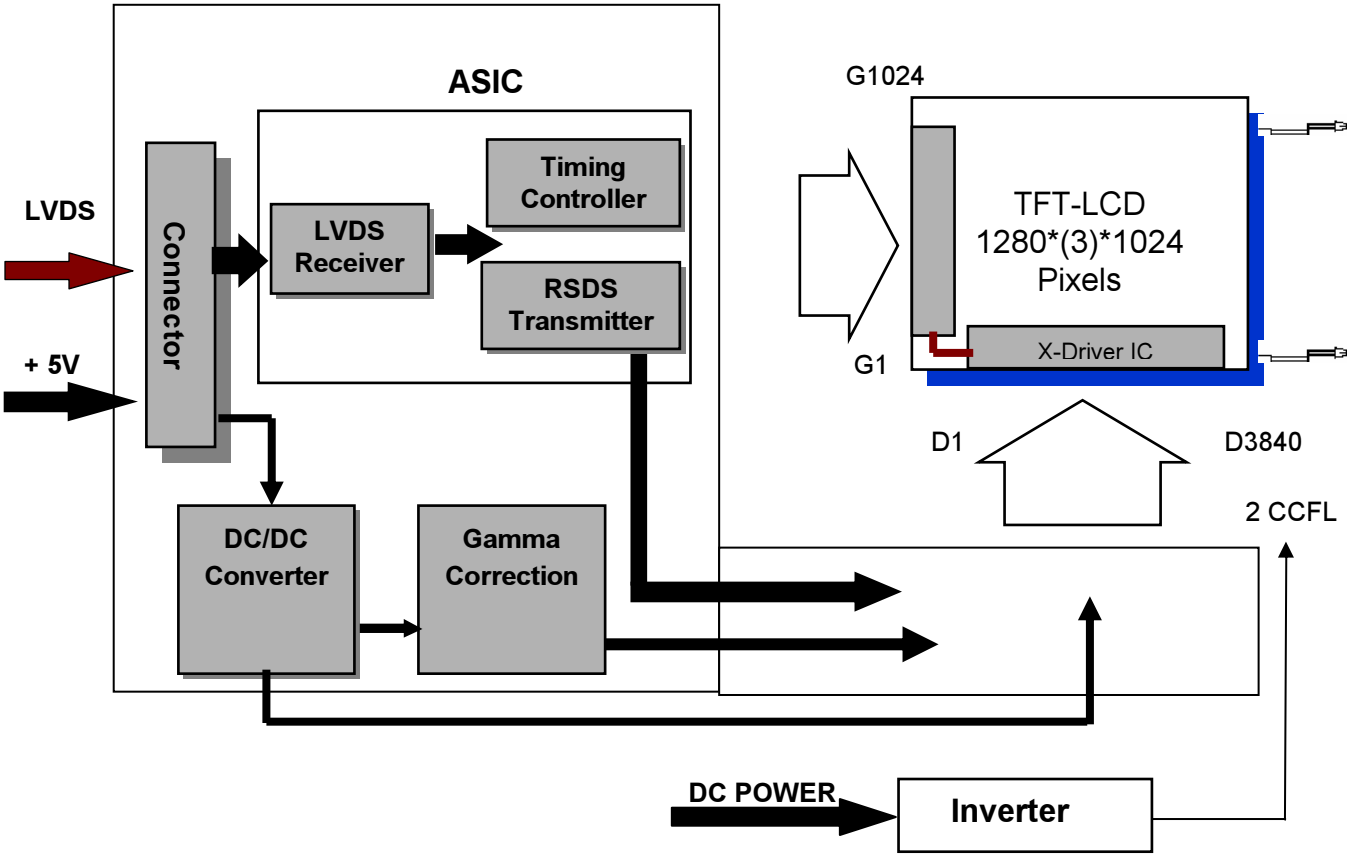
Method: Record dBV & DC value with TRD-100



$$\text{Flicker (dB)} = 20\log\frac{\text{AC Level(at 30 Hz)}}{\text{DC Level}}$$

3. Functional Block Diagram

The following diagram shows the functional block of the 19.0 inches Color TFT-LCD Module:



I/F PCB Interface:

FI-XB30SSLA-HF15 / 187034-30091

Mating Type:

FI-X30HL (Locked Type)

FI-X30H (Unlocked Type)

4. Absolute Maximum Ratings

Absolute maximum ratings of the module are as following:

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Conditions
Logic/LCD Drive Voltage	VDD	-0.3	+6	[Volt]	Note 1, 2

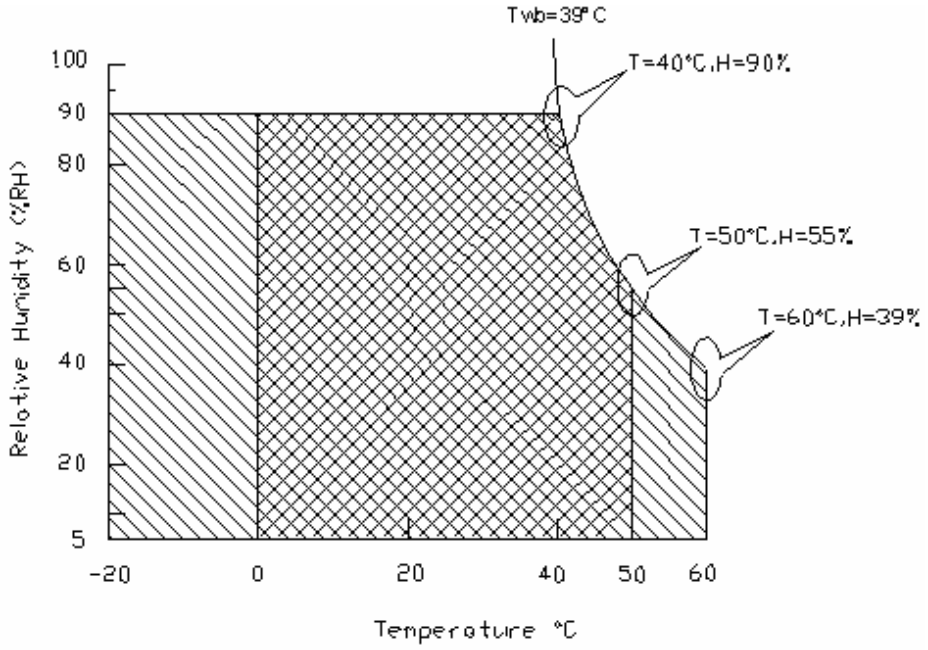
4.2 Absolute Ratings of Backlight Unit

Item	Symbol	Min.	Max.	Unit	Conditions
CCFL Current	ICFL	-	8	[mA] rms	Note 1, 2

4.3 Absolute Ratings of Environment

Item	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	0	+50	[°C]	Note 3
Operation Humidity	HOP	5	90	[%RH]	
Storage Temperature	TST	-20	+60	[°C]	
Storage Humidity	HST	5	90	[%RH]	

Note 1: With in Ta (25℃)
Note 2: Permanent damage to the device may occur if exceeding maximum values
Note 3: For quality perfermance, please refer to AUO IIS(Incoming Inspection Standard).



Operating Range Storage Range

5. Electrical characteristics

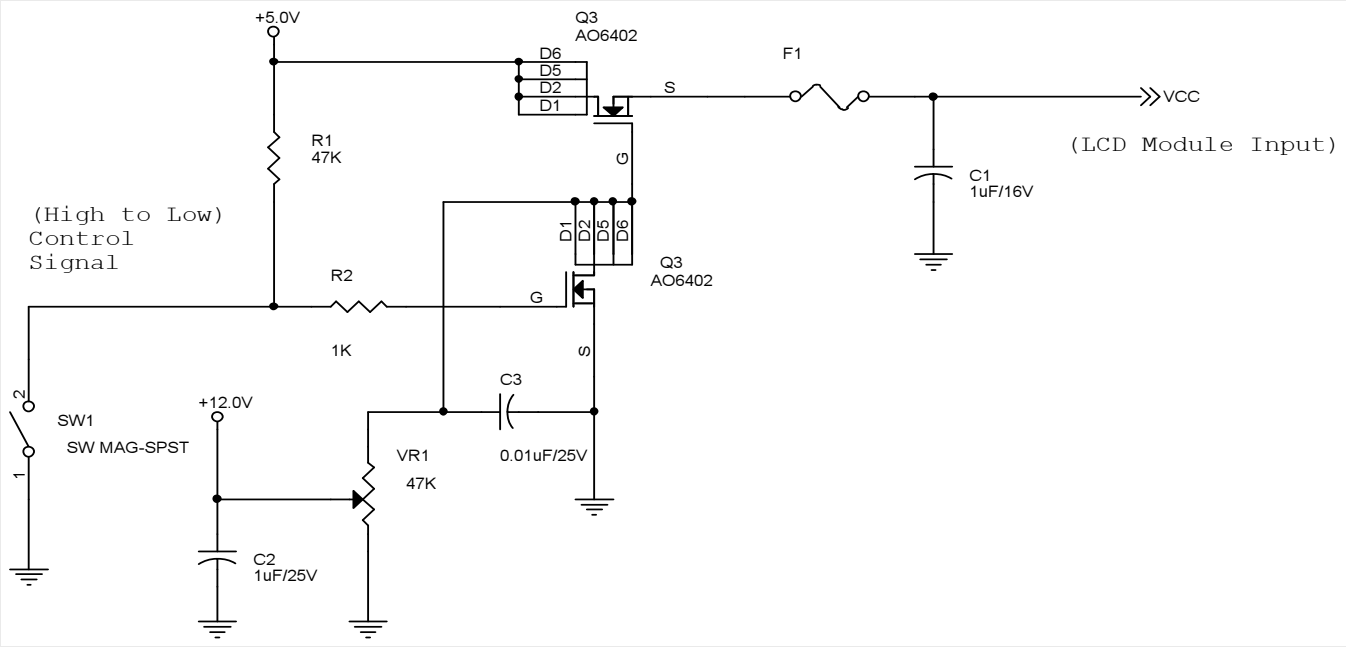
5.1 TFT LCD Module

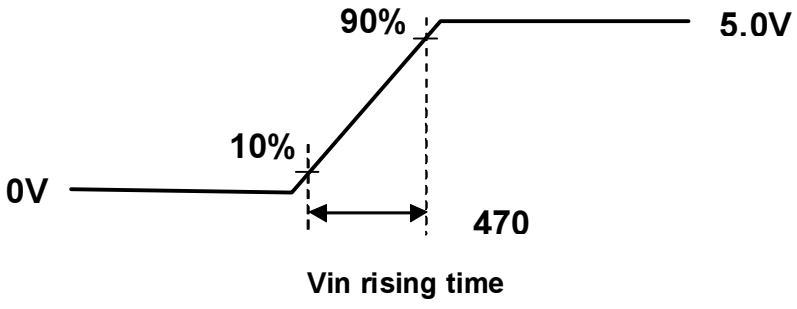
5.1.1 Power Specification

Input power specifications are as follows:

Symble	Parameter	Min.	Typ.	Max.	Unit	Condition
VCC	Logic/LCD Drive Voltage	4.5	5.0	5.5	[Volt]	±10%
ICC	Input Current	-	0.94	1.1	[A]	Vin=5V , All Black Pattern, at 60Hz
IRush	Inrush Current	-	-	3.0	[A]	Note 1
PCC	VCCPower	-	4.7	5.5	[Watt]	Vin=5V , All Black Pattern, at 60Hz
VCCrp	Allowable Logic/LCD Drive Ripple Voltage	-	–	100	[mV] p-p	With panel loading

Note 1: Measurement conditions





Vin rising time

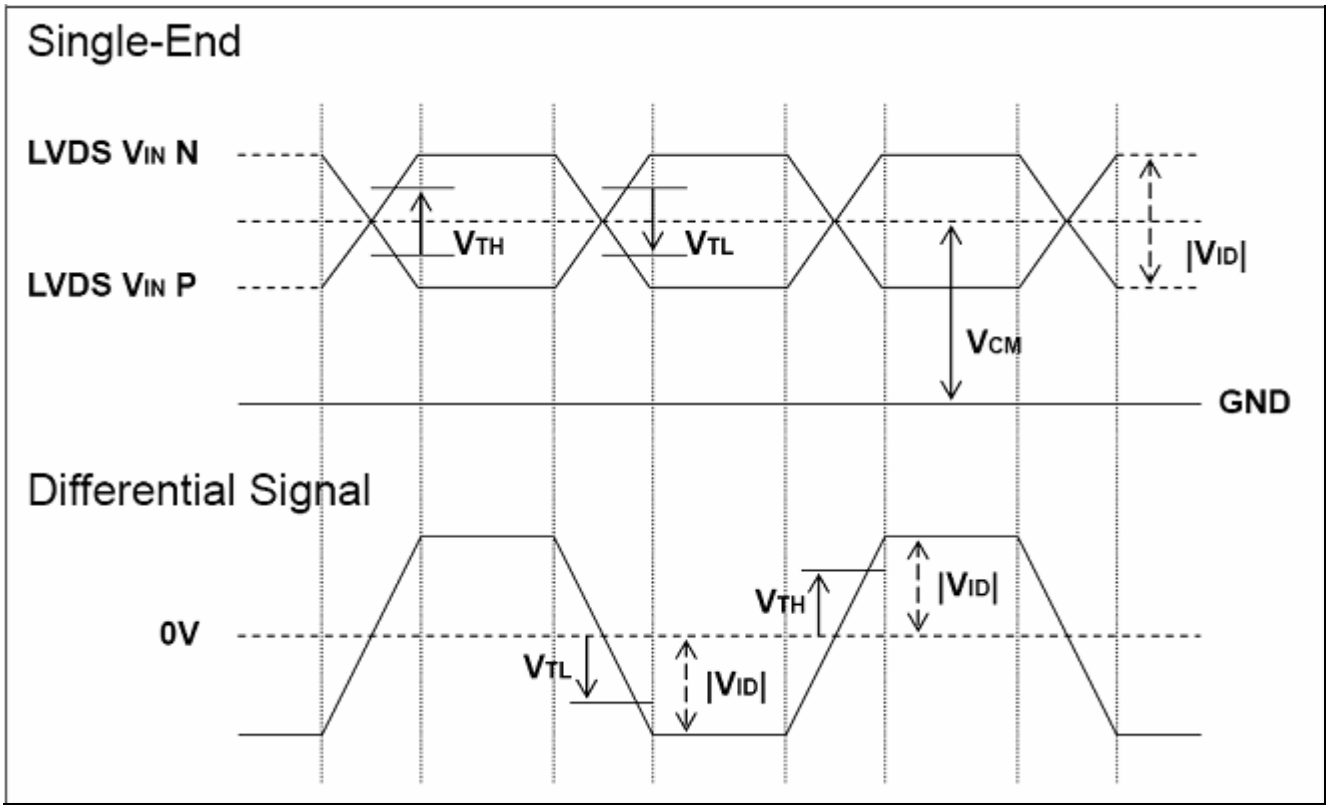
5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off. Please refer to specifications of SN75LVDS82DGG (Texas Instruments) in detail.

1. DC Characteristics of each signal are as following:

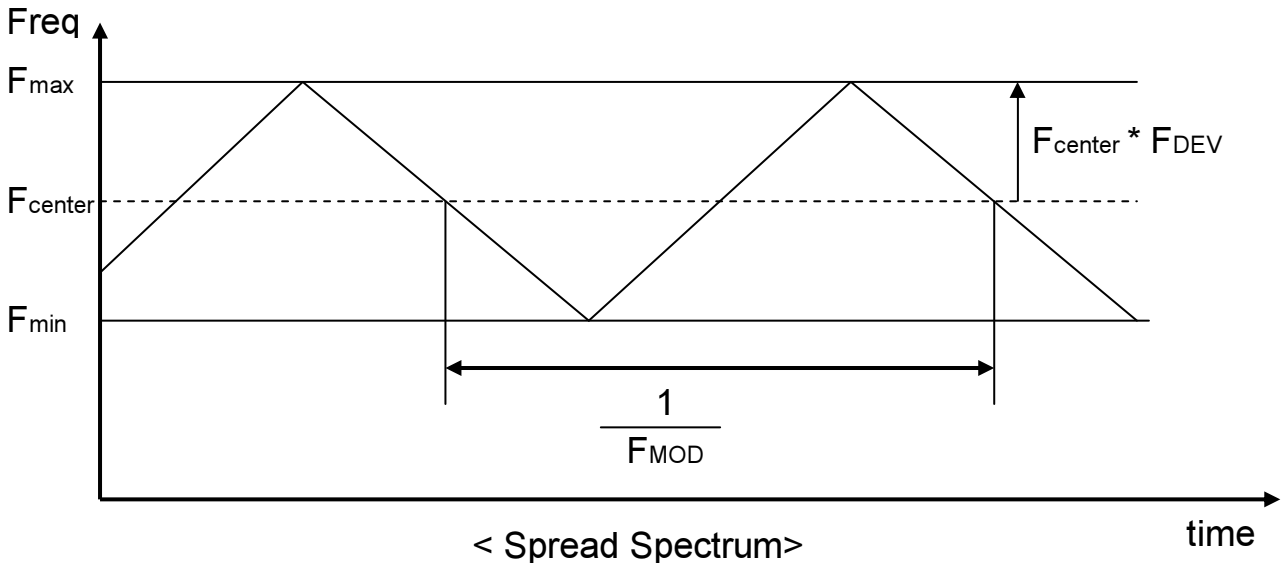
Symbol	Parameter	Min	Typ	Max	Units	Condition
V_{TH}	Differential Input High Threshold	-	-	+100	[mV]	V_{CM} = 1.2V ***Note 1***
V_{TL}	Differential Input Low Threshold	-100	-	-	[mV]	V_{CM} = 1.2V ***Note 1***
$\lvert V_{ID} \rvert$	Input Differential Voltage	100	400	600	[mV]	***Note 1***
V_{CM}	Differential Input Common Mode Voltage	$\lvert V_{ID} \rvert/2$	+1.2	2.4-$\lvert V_{ID} \rvert/2$ VDD-0.4-$\lvert V_{ID} \rvert/2$	[V]	VDDIO 3.3V VDDIO 2.5 V

Note 1: LVDS Signal Waveform



2. AC characteristic

Description	Symbol	Min	Max	Unit	Note
Maximum deviation of input clock frequency during SSC	F_{DEV}	-	± 3	%	
Maximum modulation frequency of input clock during SSC	F_{MOD}	-	200	KHz	



5.2 Backlight Unit

Parameter guideline for CCFL Inverter is under stable conditions at 25□ (Room Temperature):

Parameter	Min.□	Typ.	Max.□	Unit	Condition
CCFL Standard Current(ISCFL)	7.5	8.0	8.5	[mA] rms	Note 2
CCFL Operation Current(IRCFL)	3.0	8.0	8.5	[mA] rms	Note 2
CCFL Frequency(FCFL)	40	55	80	[KHz]	Note 3,4
CCFL Ignition Voltage(ViCFL, Ta= 0□)	1470	-	-	[Volt] rms	Note 5
CCFL Ignition Voltage(ViCF, Ta= 25□)	1130	-	-	[Volt] rms	
CCFL Operation Voltage (VCFL)	-	605 (@8.0 mA)	-	[Volt] rms	Note 6
CCFL Power Consumption(PCFL)	-	9.68	-	[Watt]	Note 6
CCFL Life Time(LTCFL)	50000	-	-	[Hour]	Note 7

Note 1: Typ. are AUO recommended design points.

*1 All of characteristics listed are measured under the condition using the AUO test inverter.

*2 In case of using an inverter other than listed, it is recommended to check the inverter carefully. Sometimes, interfering noise stripes appear on the screen, and substandard luminance or flicker at low power may happen.

*3 In designing an inverter, it is suggested to check safety circuit very carefully. Impedance of CCFL, for instance, becomes more than 1 [M ohm] when CCFL is damaged.

*4 Generally, CCFL has some amount of delay time after applying kick-off voltage. It is recommended to keep on applying kick-off voltage for 1 [Sec] until discharge.

*5 Reducing CCFL current increases CCFL discharge voltage and generally increases CCFL discharge frequency. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

Note 2: CCFL standard current is measured at 25±2℃.

Note 3: CCFL discharge frequency should be carefully determined to avoid interference between inverter and TFT LCD.

Note 4: The frequency range will not affect to lamp life and reliability characteristics.

Note 5: CCFL inverter should be able to give out a power that has a generating capacity of over 1,650 voltage. Lamp units need 1,650 voltage minimum for ignition.

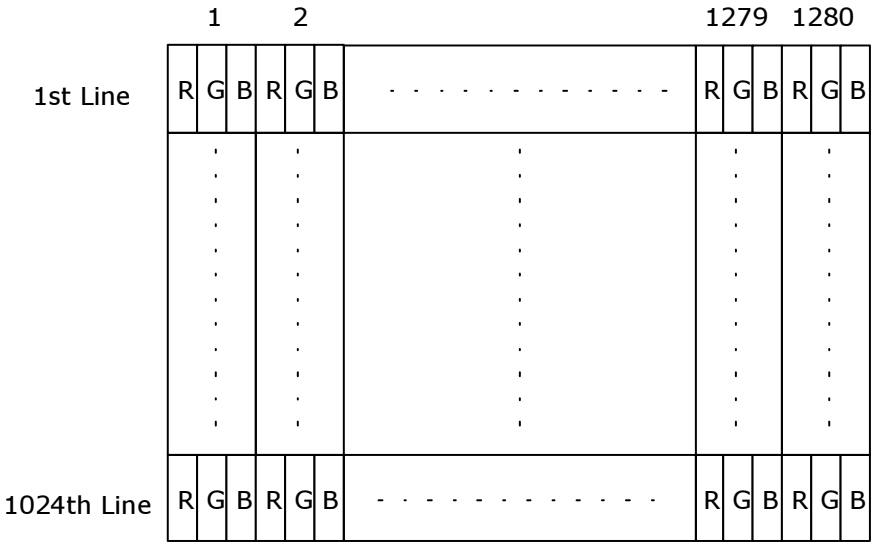
Note 6: The variance of CCFL power consumption is ±10%. Calculator value for reference (ISCFL × VCFL × 2 = PCFL)

Note 7: Definition of life: brightness becomes 50%. The typical life time of CCFL is on the condition at 8.0 mA lamp current and 25±2℃..

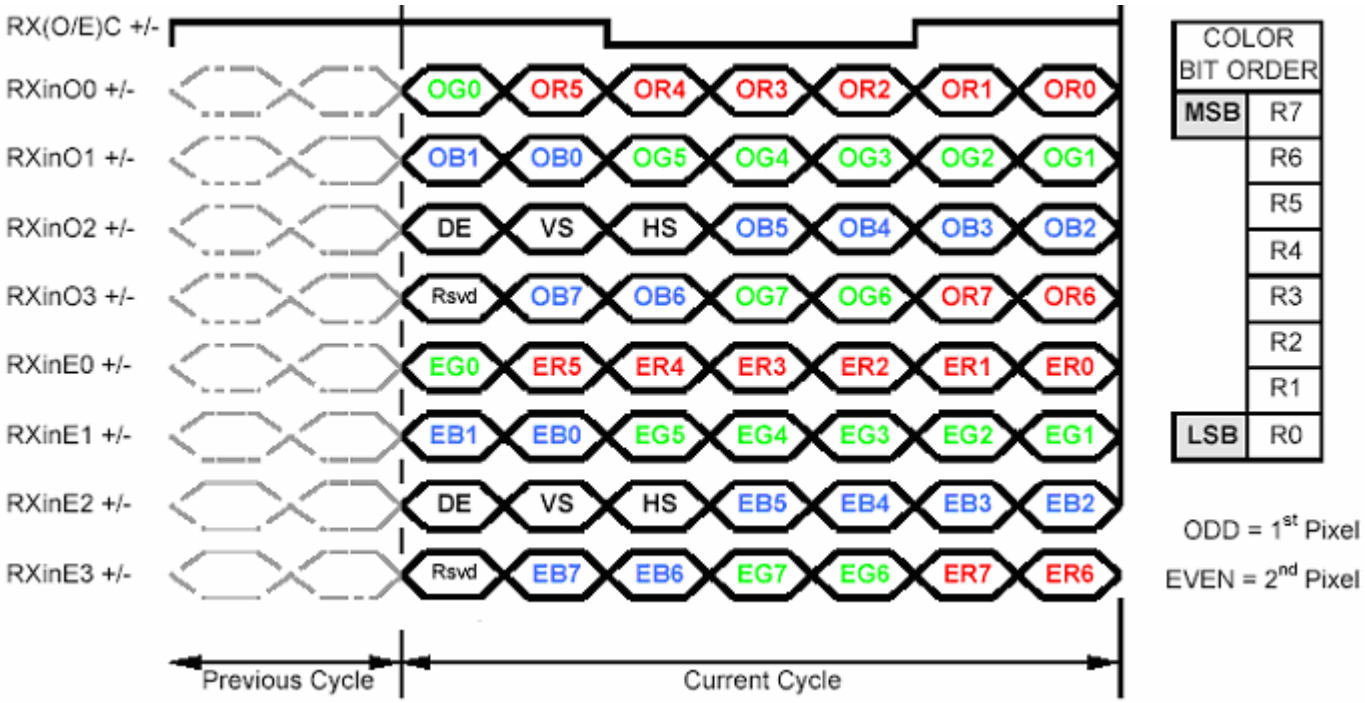
6. Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.



6.2 The Input Data Format



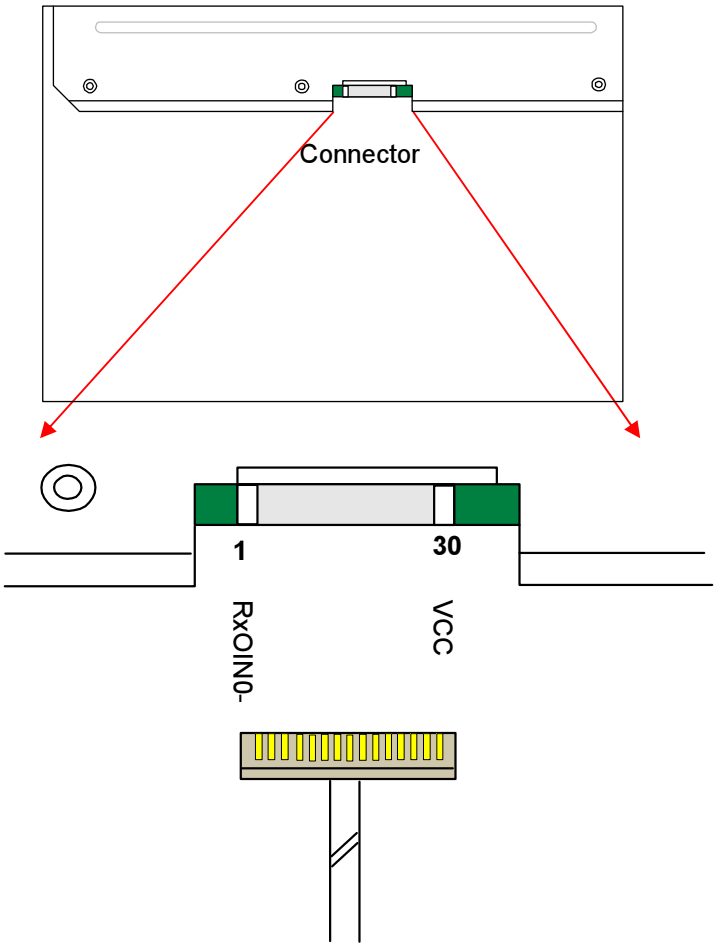
Note1: Normally, DE, VS, HS on EVEN channel are not used.
Note2: Please follow PSWG.
Note3: 8-bit in

6.3 Signal Description

The module using a pair of LVDS receiver SN75LVDS82(Texas Instruments) or compatible. LVDS is a differential signal technology for LCD interface and high speed data transfer device. Transmitter shall be SN75LVDS83(negative edge sampling) or compatible. The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels.

PIN #	SIGNAL NAME	DESCRIPTION
1	RxOIN0-	Negative LVDS differential data input (Odd data)
2	RxOIN0+	Positive LVDS differential data input (Odd data)
3	RxOIN1-	Negative LVDS differential data input (Odd data)
4	RxOIN1+	Positive LVDS differential data input (Odd data)
5	RxOIN2-	Negative LVDS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)
6	RxOIN2+	Positive LVDS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)
7	VSS	Power Ground
8	RxOCLKIN-	Negative LVDS differential clock input (Odd clock)
9	RxOCLKIN+	Positive LVDS differential clock input (Odd clock)
10	RxOIN3-	Negative LVDS differential data input (Odd data)
11	RxOIN3+	Positive LVDS differential data input (Odd data)
12	RxEIN0-	Negative LVDS differential data input (Even data)
13	RxEIN0+	Positive LVDS differential data input (Even data)
14	VSS	Power Ground
15	RxEIN1-	Negative LVDS differential data input (Even data)
16	RxEIN1+	Positive LVDS differential data input (Even data)
17	VSS	Power Ground
18	RxEIN2-	Negative LVDS differential data input (Even data)
19	RxEIN2+	Positive LVDS differential data input (Even data)
20	RxECLKIN-	Negative LVDS differential clock input (Even clock)
21	RxECLKIN+	Positive LVDS differential clock input (Even clock)
22	RxEIN3-	Negative LVDS differential data input (Even data)
23	RxEIN3+	Positive LVDS differential data input (Even data)
24	VSS	Power Ground
25	VSS	Power Ground
26	NC	Do not connect (for AUO test)
27	VSS	Power Ground
28	VCC	+5.0V Power Supply
29	VCC	+5.0V Power Supply
30	VCC	+5.0V Power Supply

Note1: Start from left side



Note2: Input signals of odd and even clock shall be the same timing.

Note3: Please follow PSWG.

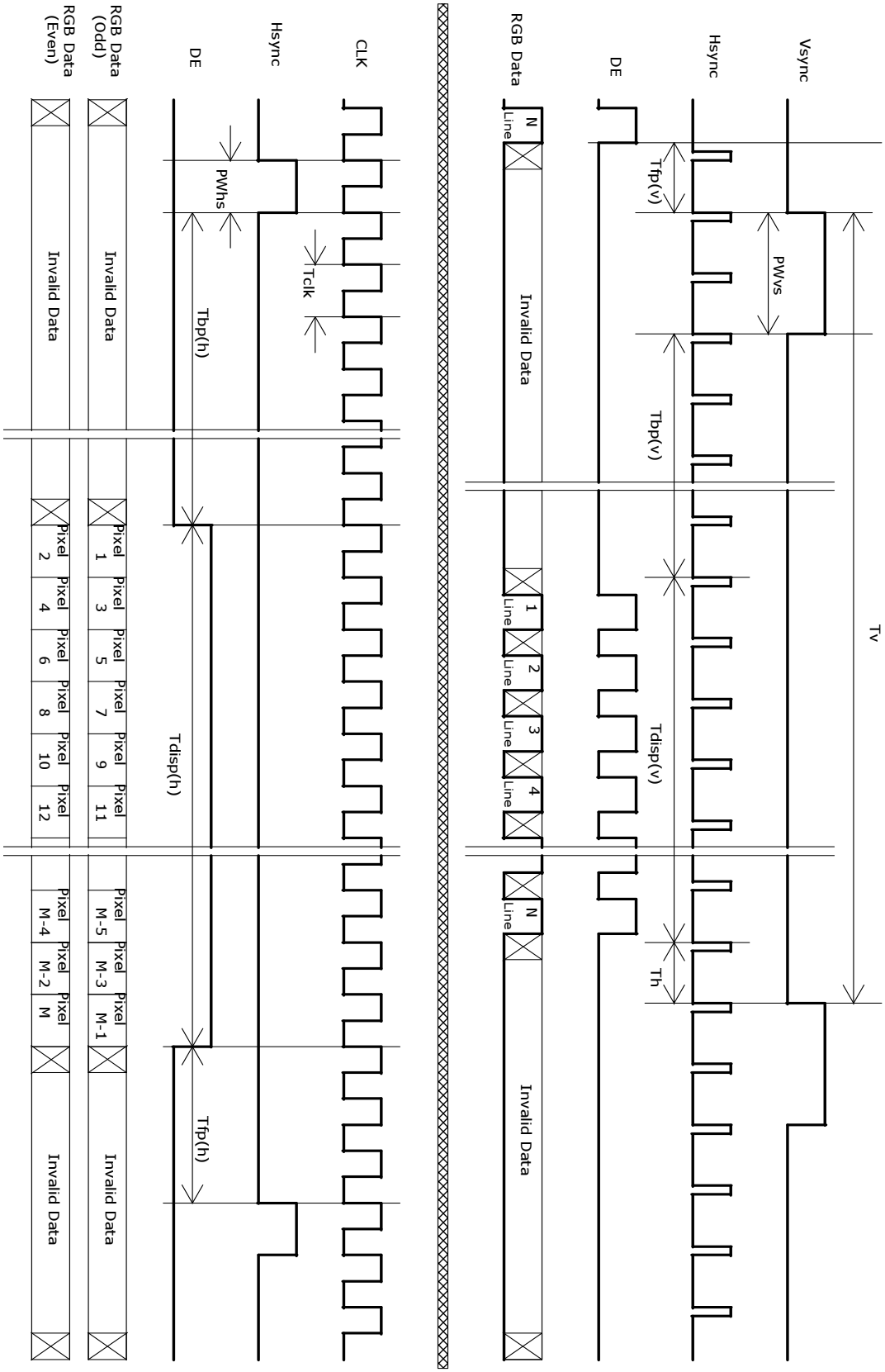
6.4 Interface Timing

6.4.1 Timing Characteristics

Signal	Item	Symbol	Min	Typ	Max	Unit
Vertical Section	Period	Tv	1032	1066	1150	Th
	Active	Tdisp(v)	1024	1024	1024	Th
	Blanking	Tbp(v)+Tfp(v)+PWvs	8	42	126	Th
Horizontal Section	Period	Th	780	844	2047	Tclk
	Active	Tdisp(h)	640	640	640	Tclk
	Blanking	Tbp(h)+Tfp(h)+PWhs	140	204	-	Tclk
Clock	Period	Tclk	22.2	18.52	14.81	ns
	Frequency	Freq.	45	54	67.5	MHz
Frame Rate	Frequency	1/Tv	50	60	75	Hz

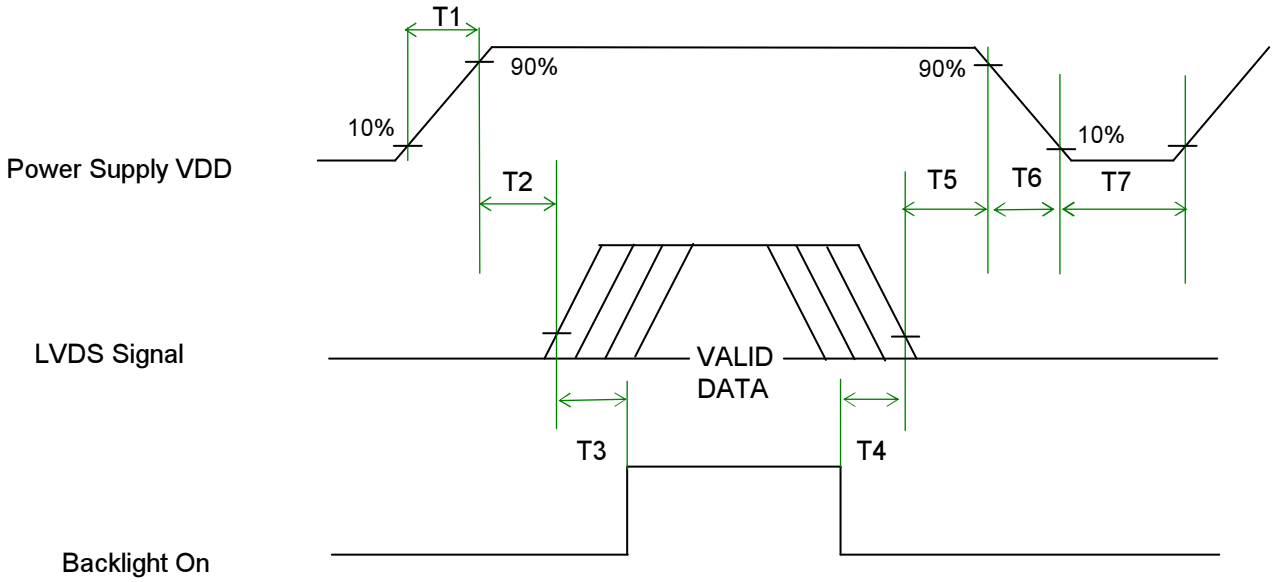
Note : DE mode only

6.4.2 Timing Diagram



6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power Sequence Timing

Parameter	Value			Unit
	Min.	Typ.	Max.	
T1	0.5	-	10	[ms]
T2	0	40	50	[ms]
T3	300	-	-	[ms]
T4	300	-	-	[ms]
T5	0.5	16	50	[ms]
T6	-	-	-	[ms]
T7	1000	-	-	[ms]

7. Connector & Pin Assignment

Physical interface is described as for the connector on module.These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

7.1.1 Connector

Connector Name / Designation	Interface Connector / Interface card
Manufacturer	JAE / P-TWO
Type Part Number	FI-XB30SSLA-HF15 / 187034-30091
Mating Housing Part Number	FI-X30HL FI-X30H (Unlocked Type)

7.1.2 Pin Assignment

Pin#	Signal Name	Pin#	Signal Name
1	RxOIN0-	2	RxOIN0+
3	RxOIN1-	4	RxOIN1+
5	RxOIN2-	6	RxOIN2+
7	VSS	8	RxOCLKIN-
9	RxOCLKIN+	10	RxOIN3-
11	RxOIN3+	12	RxEIN0-
13	RxEIN0+	14	VSS
15	RxEIN1-	16	RxEIN1+
17	VSS	18	RxEIN2-
19	RxEIN2+	20	RxECLKIN-
21	RxECLKIN+	22	RxEIN3-
23	RxEIN3+	24	VSS
25	VSS	26	NC
27	VSS	28	VCC
29	VCC	30	VCC

7.2 Backlight Unit

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

Connector Name / Designation	Lamp Connector / Backlight lamp
Manufacturer	CVILUX / YEONHO
Type Part Number	CP0502SL090/ 35001 HS-02L
Mating Type Part Number	CP0502P1ML0/ 35001 WR-02L

7.2.1 Signal for Lamp connector

	Connector No.	Pin No.	Input	Color	Function
Upper	CN1	1	Hot1	Pink	High Voltage
		2	Cold1	White	Low Voltage

	Connector No.	Pin No.	Input	Color	Function
Lower	CN2	1	Hot1	Pink	High Voltage
		2	Cold1	White	Low Voltage

8. Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50□, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50□, 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0□, 300hours	
High Temperature Storage (HTS)	Ta= 60□, 300hours	
Low Temperature Storage (LTS)	Ta= -20□, 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 G Wave: Random Frequency: 10 - 200 - 10 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Drop Test	Height: 60 cm, package test	
Thermal Shock Test (TST)	-20□/30min, 60□/30min, 100 cycles	1
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (ElectroStatic Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 9 points, 25 times/ point.	2
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 9 points, 25 times/ point.	
Altitude Test	Operation:10,000 ft Non-Operation:30,000 ft	

Note 1: The TFT-LCD module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20℃ to 60℃, and back again. Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 2: According to EN61000-4-2 , ESD class B:

Some performance degradation allowed.

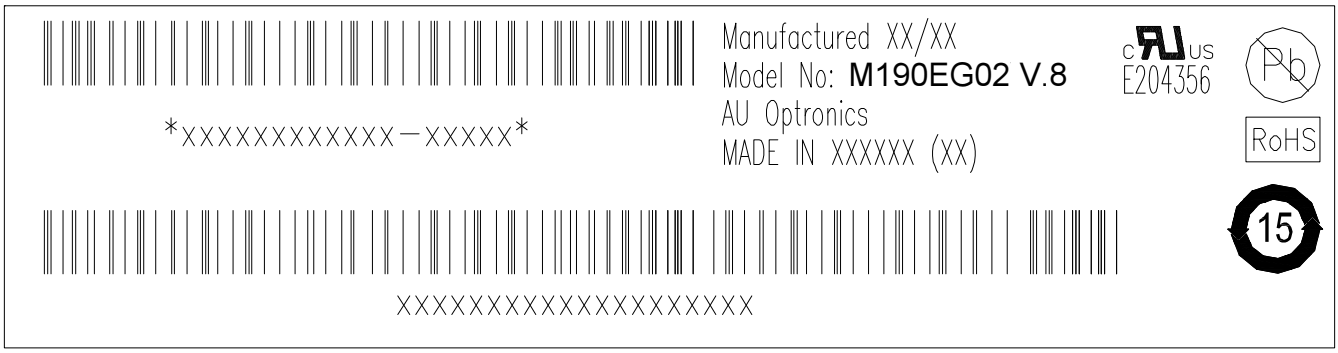
No data lost.

Self-recoverable.

No hardware failures.

9. Shipping Label

The shipping label format is shown as below.



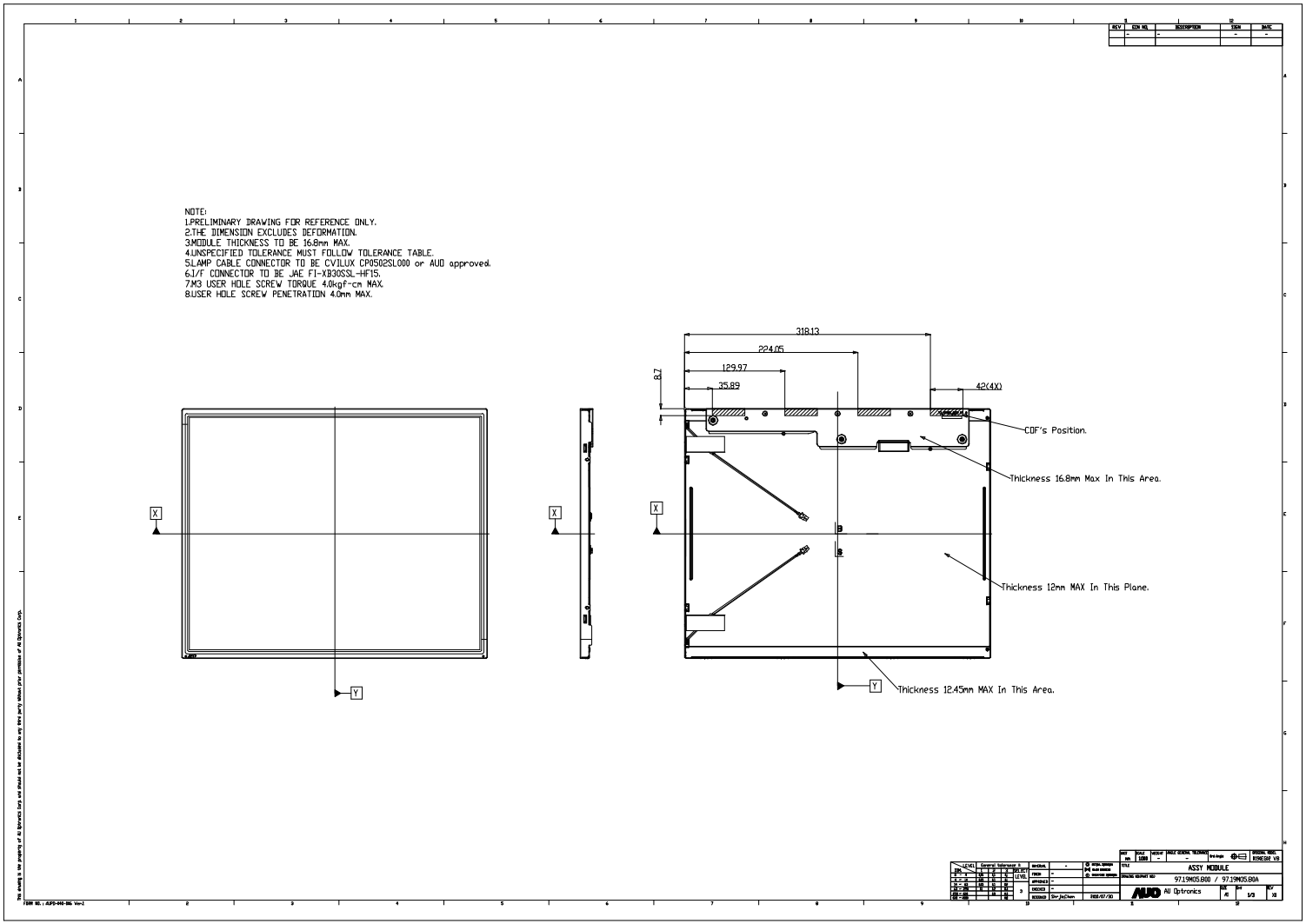
Note 1: For Pb Free products, AUO will add for identification.

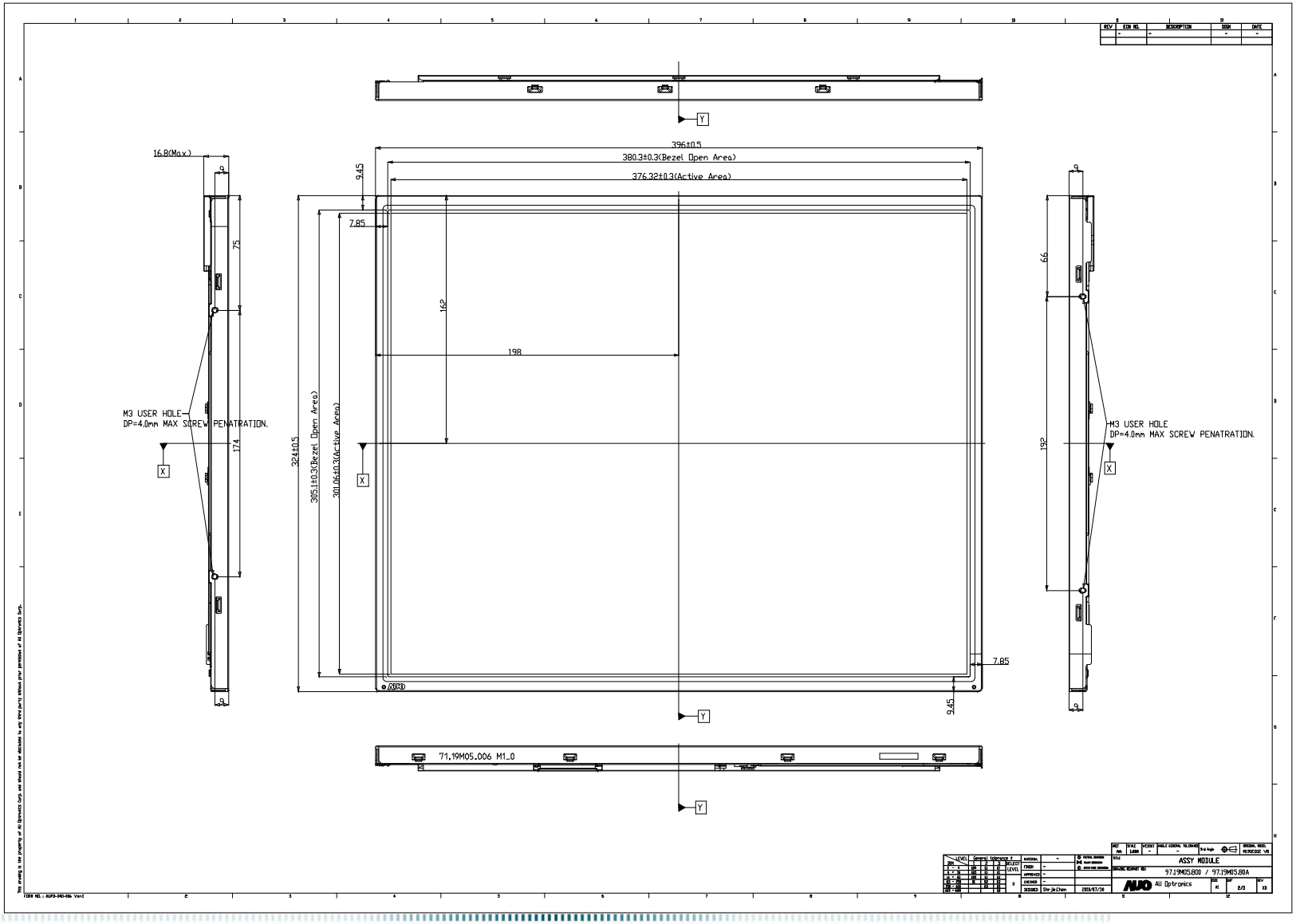
Note 2: For RoHS compatible products, AUO will add for identification.

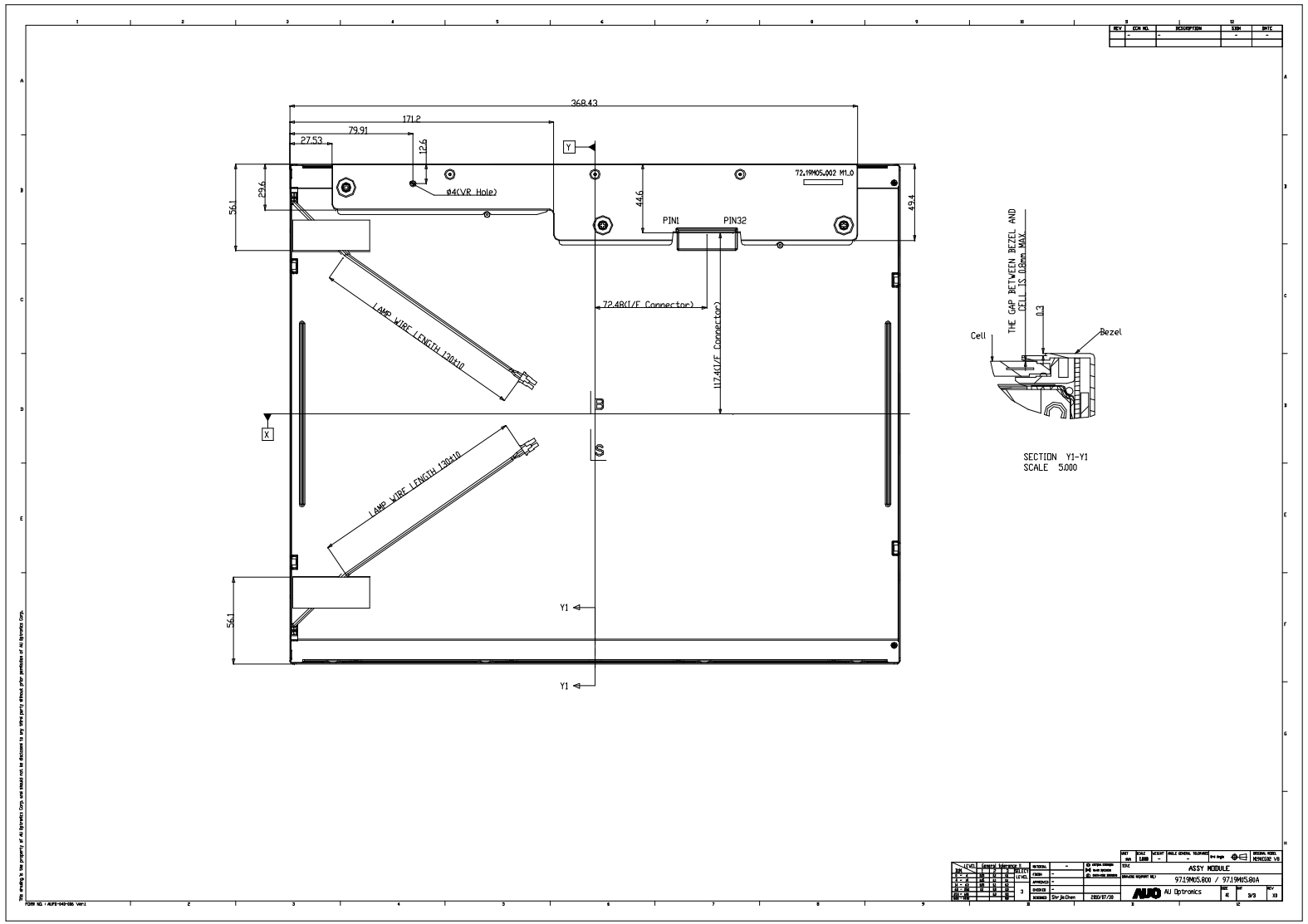
Note 3: For China RoHS compatible products, AUO will add for identification.

Note 4: The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

10. Mechanical Characteristic




16.8(Max.)
75
174
M3 USER HOLE
DP=4.0mm MAX SCREW PENATRATION.
396±0.5
380.3±0.3(Bezel Open Area)
376.32±0.3(Active Area)
9.45
7.85
162
198
324±0.5
305.1±0.3(Bezel Open Area)
301.06±0.3(Active Area)
7.85
9.45
66
132
M3 USER HOLE
DP=4.0mm MAX SCREW PENATRATION.
71.19M05.006 M1_0
ASSY MODULE
97.19M05.B00 / 97.19M05.B0A
AU Optronics


368.43
171.2
79.91
27.53
12.6
Y
72.19M05.002 M1.0
Ø4(VR Hole)
29.6
56.1
44.6
49.4
PIN1
PIN32
72.48(I/F Connector)
117.40(I/F Connector)
LAMP WIRE LENGTH 130±10
LAMP WIRE LENGTH 130±10
X
S
Y1
Y1
THE GAP BETWEEN BEZEL AND CELL IS 0.8mm MAX.
0.3
Cell
Bezel
SECTION Y1-Y1
SCALE 5.000
ASSY MODULE
97.19M05.B00 / 97.19M05.B0A